IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of	ᆔ)
In re Patent Application of Soichi Kobayashi et al.	Group Art Unit: 2117
Application No.: 10/632,928	Examiner: SAQIB JAVAID SIDDIQUI
Filing Date: August 4, 2003) Confirmation No.: 4942
Title: SEMICONDUCTOR INTEGRATED CIRCUIT CAPABLE OF TESTING WITH SMALL SCALE CIRCUIT CONFIGURATION)))
PETITION FOI	R EXTENSION OF TIME
Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450	
Sir:	
The following extension of time is requated July 10, 2007 for	uested to: respond to the outstanding Office Action
Three Months to January 10, 2008	
The shortened statutory period	d has been reset by an Advisory Action dated
An Extension fee in the amour	nt of is enclosed.
Charge to D	eposit Account No. 02-4800.
Charge \$ 1050.00 to credit car	rd. Form PTO-2038 is attached.
The Director is hereby authorized to c 1.17 and 1.21 that may be required by this pa Account No. 02-4800. This paper is submitte	harge any appropriate fees under 37 C.F.R. §§1.16, aper, and to credit any overpayment, to Deposit od in duplicate.
Re	espectfully submitted,
Date: January 9, 2008 By	Jonathan Bowser
•	Registration No. 54:57(4:2008 JADD01 09000043 10632928
P.O. Box 1404 Alexandria, VA 22313-1404 703 836 6620	91 FC:1253 1050.00 OP